

### **ABSTRACT OF THE DISCLOSURE**

A method and circuit for performing an on-board test of connections between field programmable gate arrays (FPGAs). By the method, circuit structures can be adjusted according to the width of a bus, simplifying connection testing between on-board FPGAs. Moreover, bus quality and speed can be functionally tested by the method. Because FPGAs are reprogrammable, the present invention can be employed without additional cost.